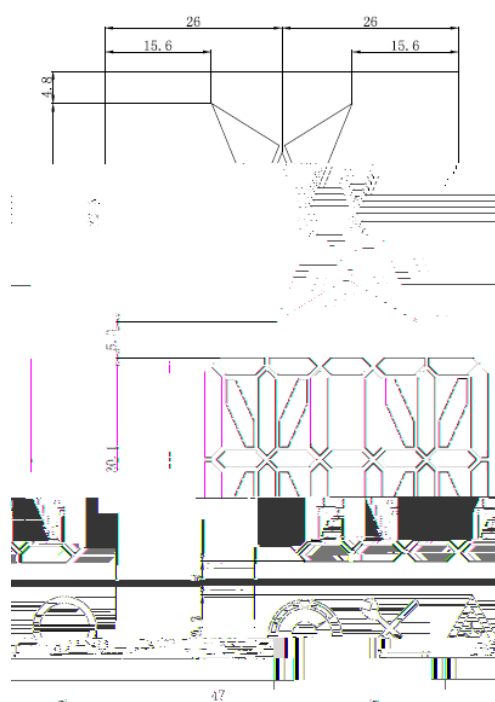
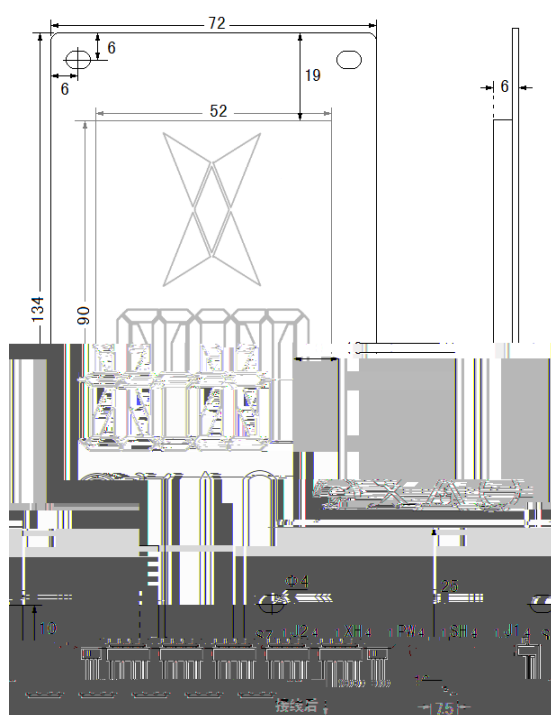


	BL2000-HEH-N4.X	
PCB	134mm*72mm*7.5mm	
		PCB
BL2000-HEH-N4.X B/C/D		



mm



--	--	--	--

```

graph TD
    R[64-bit Register]
    R -- "1, SH" --> O1
    O1["1: XN SZ 2"] --> R
    R -- "2, SZ" --> O2
    O2["2: SZ"] --> R
    R -- "3, 1" --> O3
    O3["3: 1"] --> R
    R -- "4, 64" --> O4
    O4["4: 64"] --> R
    R -- "5, 33" --> O5
    O5["5: 33"] --> R
    R -- "0" --> End
    style End fill:none,stroke:none

```

3.2 2

1				
2	10		5	
3				5

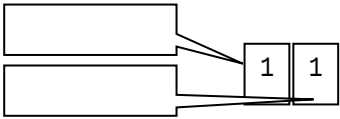
4.1

JC SZ

4.2

U P

3



1

1

30

4.3

5.15

“ ”

5.16

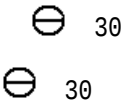
4.4

JC EN

5.1 0



N 1
2
3

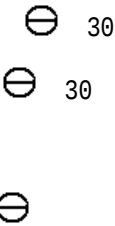


1

5.2 1



N 1
2
3
4

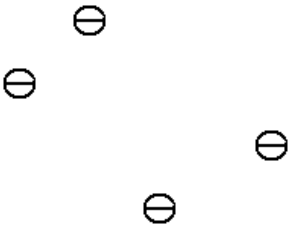


2

5.3 2



N 1
2
3
4



3

F n u o

5.4 3



N 1
2



2

5.5 4



N 1
2



2

5.6

5.12 B

15

B	N
---	---

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
N 0	A	B	C	D	E	F	G	H	I	J		L			O
N 1	A	B	C	D	E	0	1	2	3	4	5	6	7	8	9

0

5.13 C

C	N
---	---

N 0

1

0

5.14 D

N 0

1

1

5.15 E

N 0

1

0 BL2000-HEH-N4.4

5.16 S

3 N N 3 0

5.17 T

T	
---	--

3

N

N 3 0